

FIG. 1

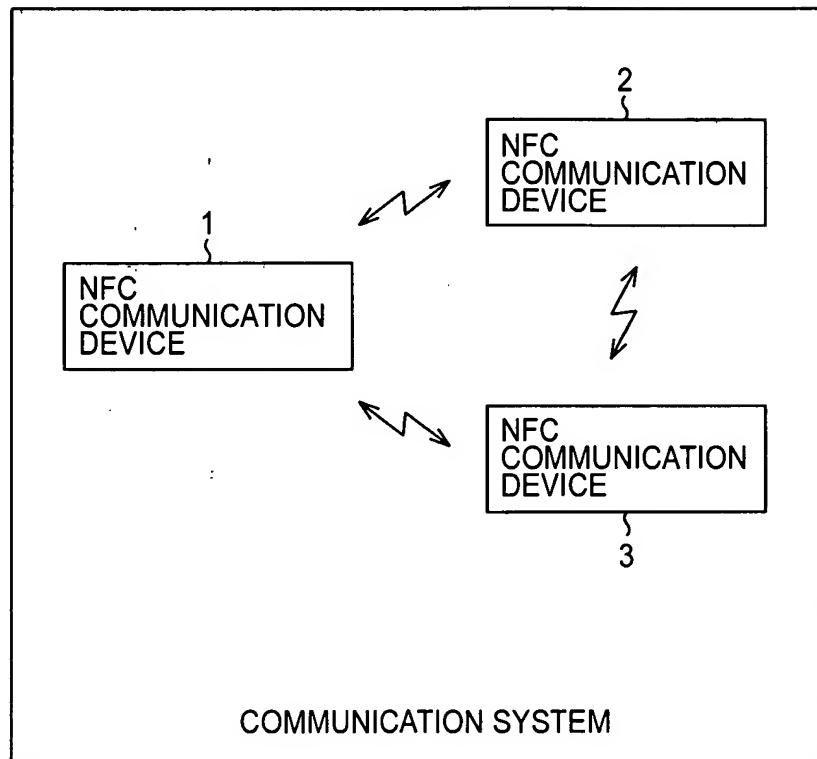


FIG. 2

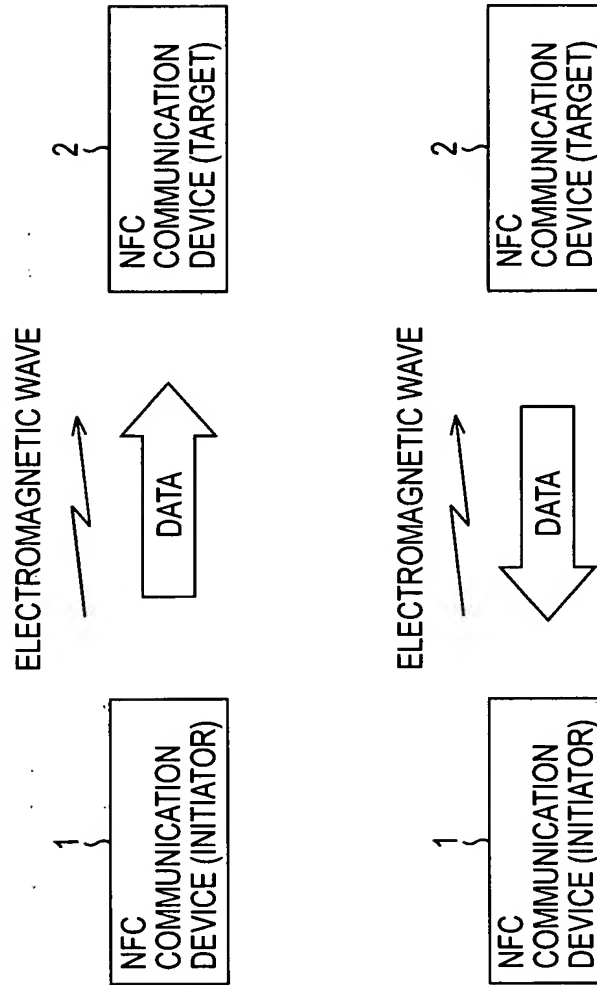


FIG. 3

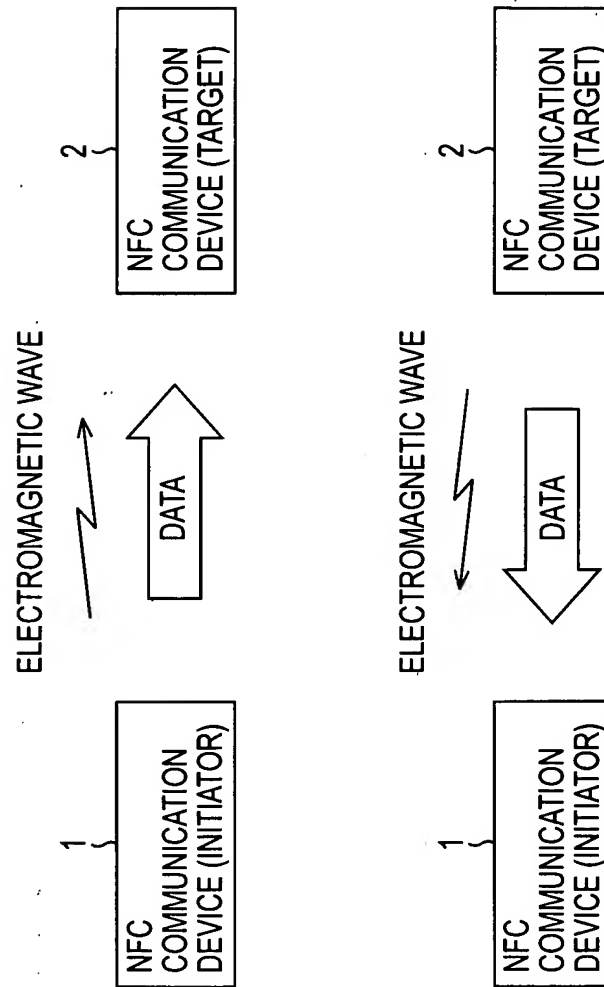
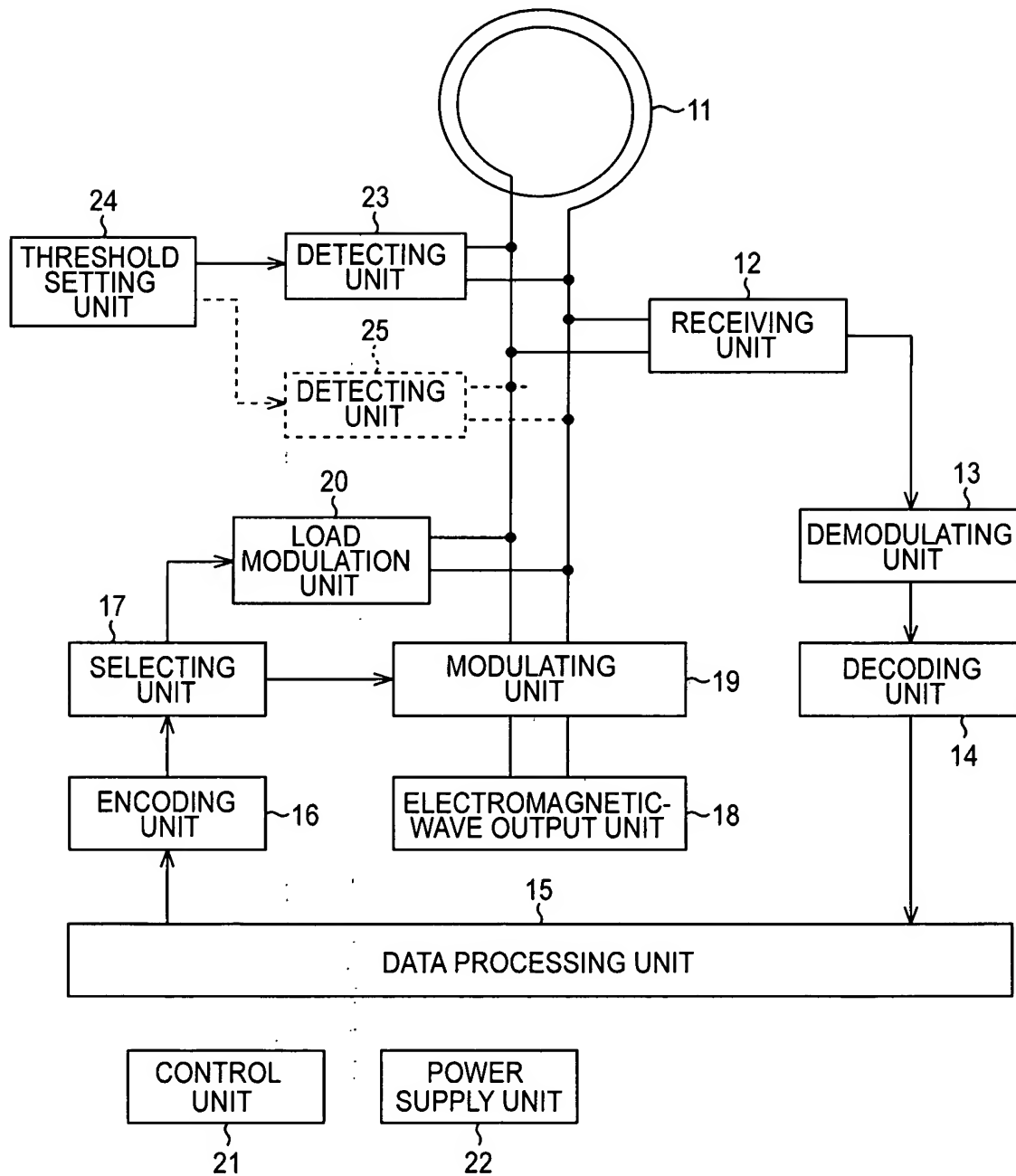


FIG. 4



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FIG. 5

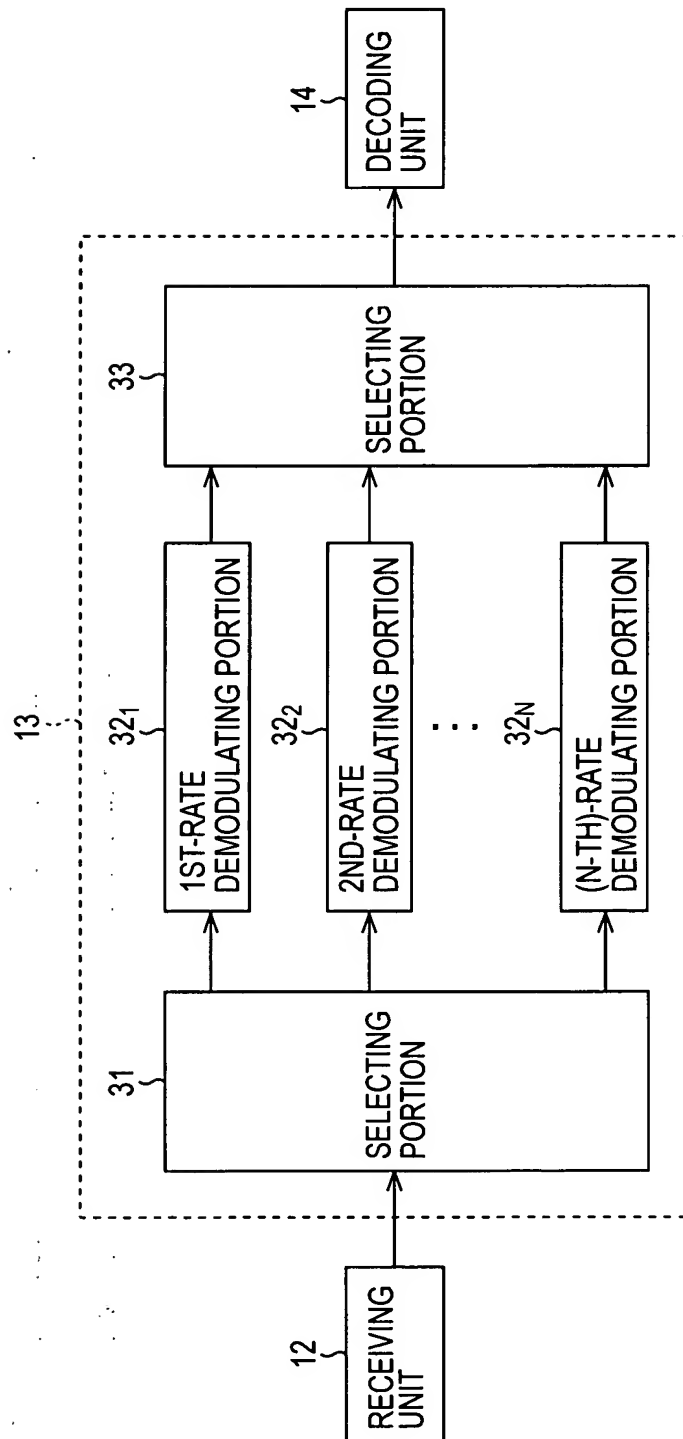
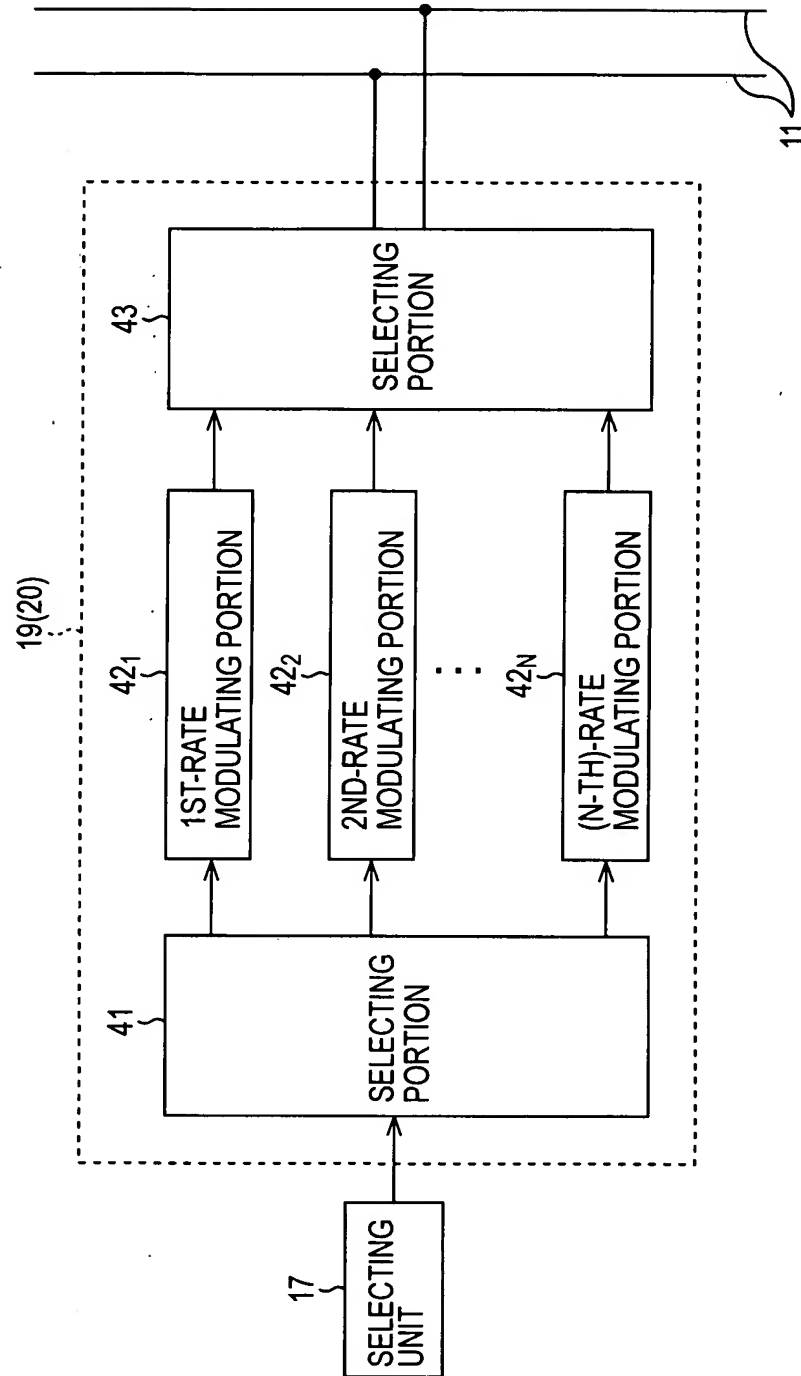


FIG. 6



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FIG. 7

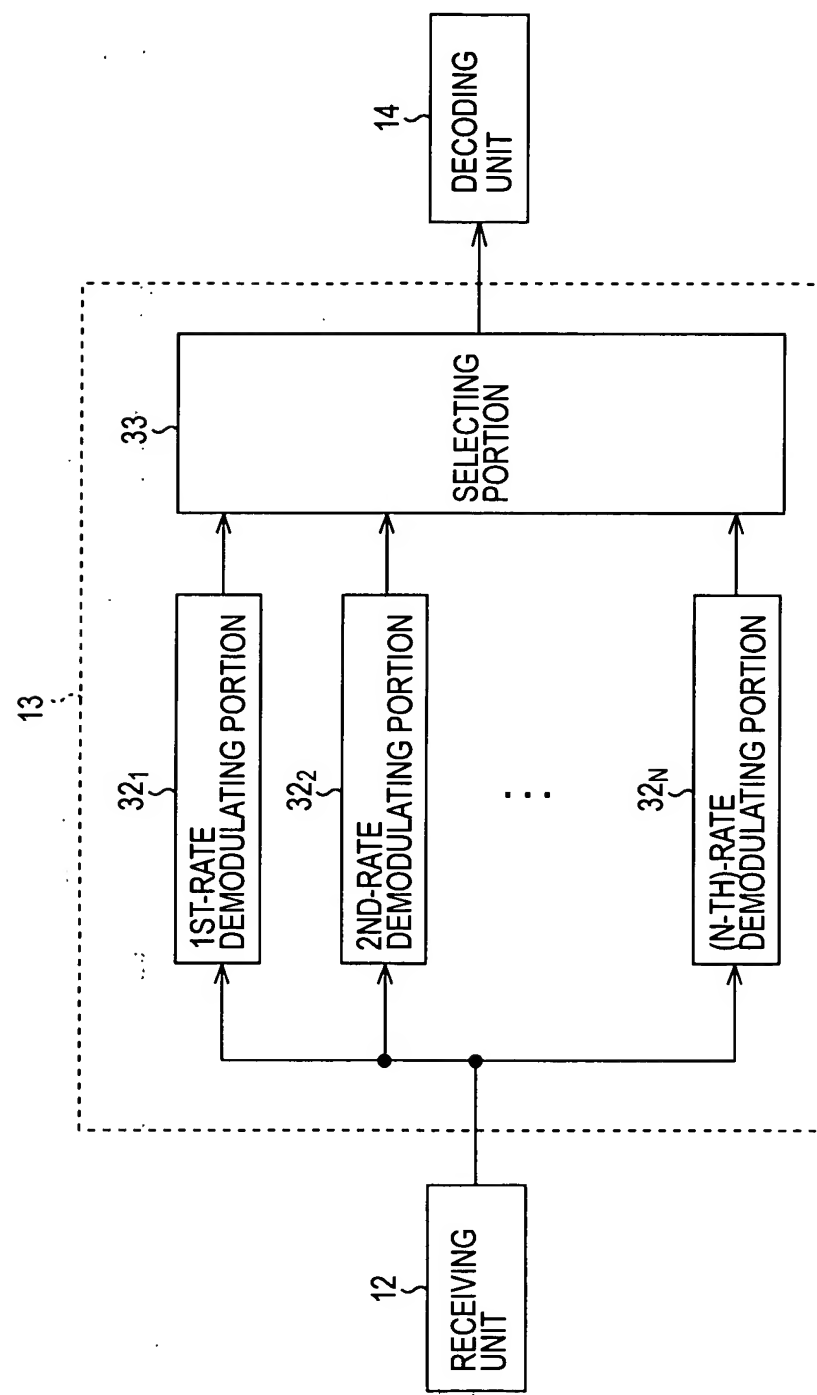


FIG. 8

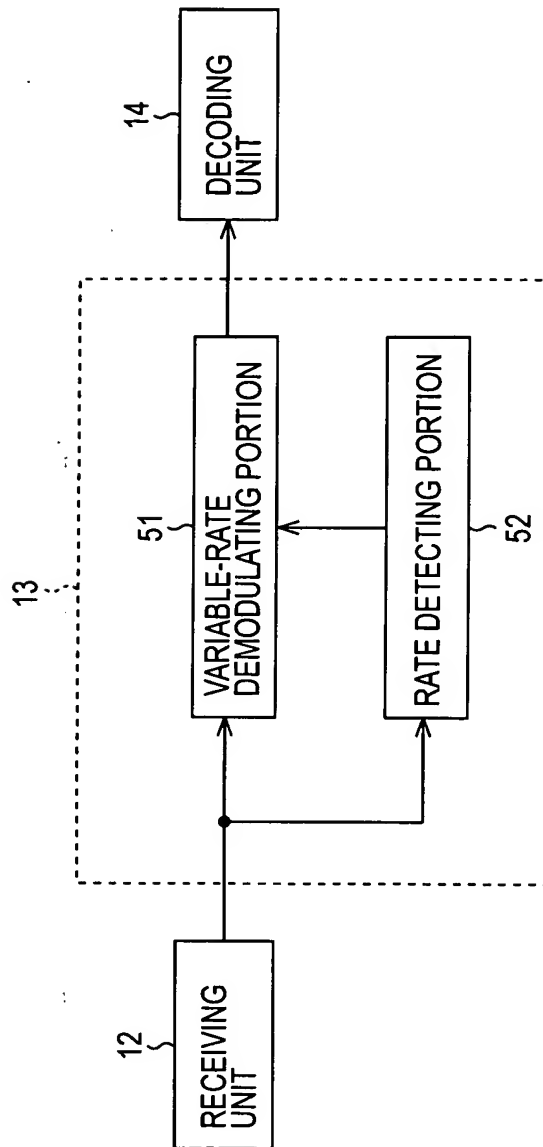


FIG. 9

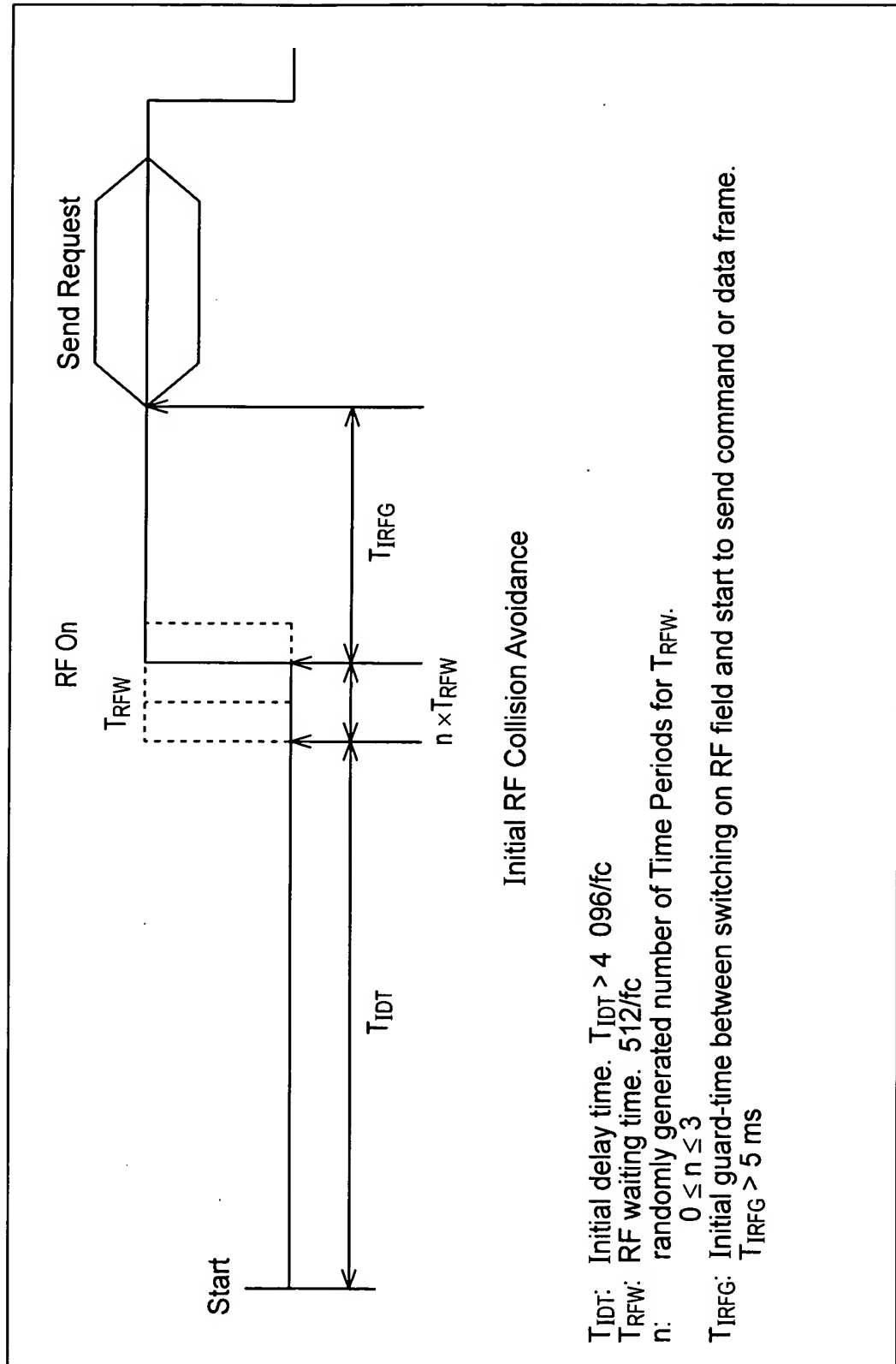
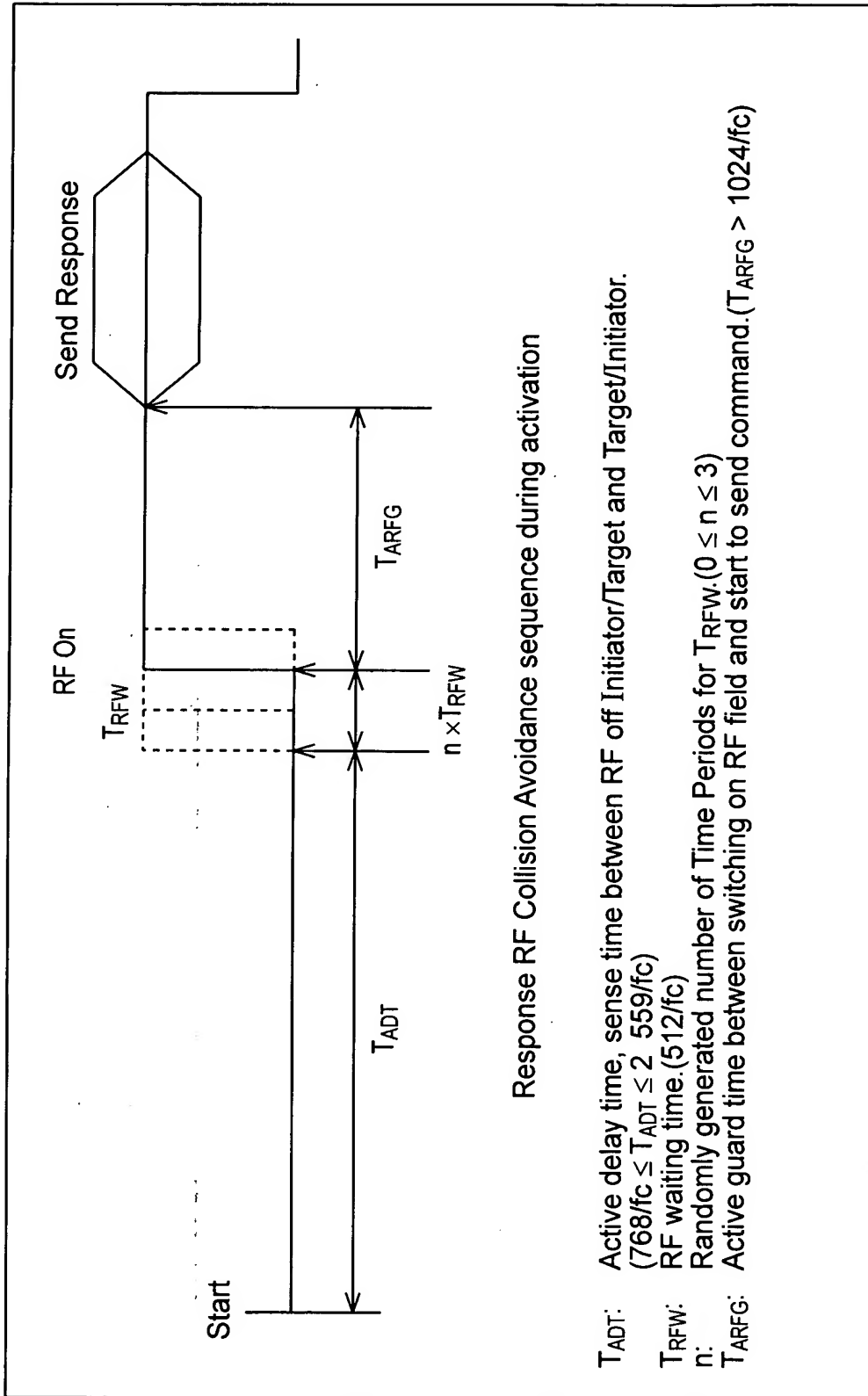


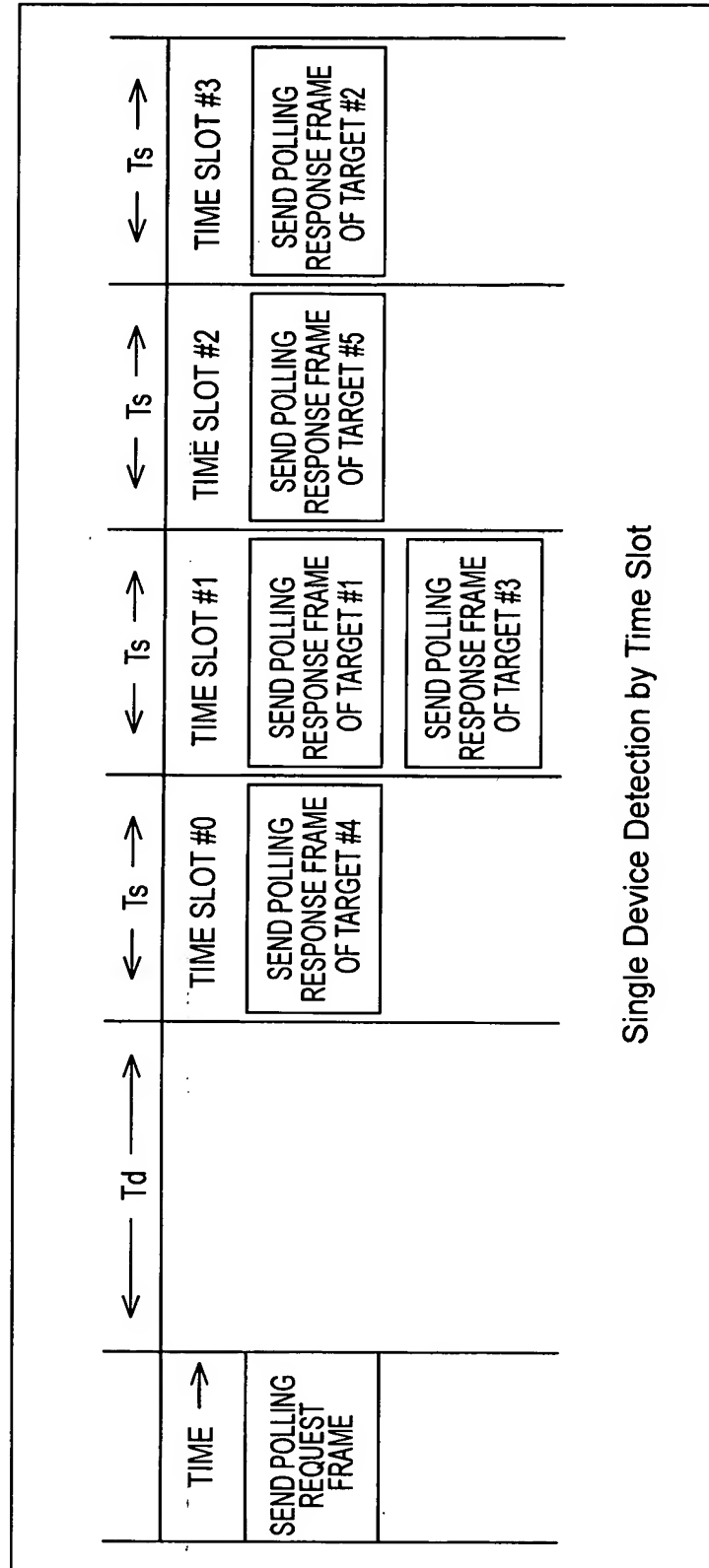
FIG. 10



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FIG. 11



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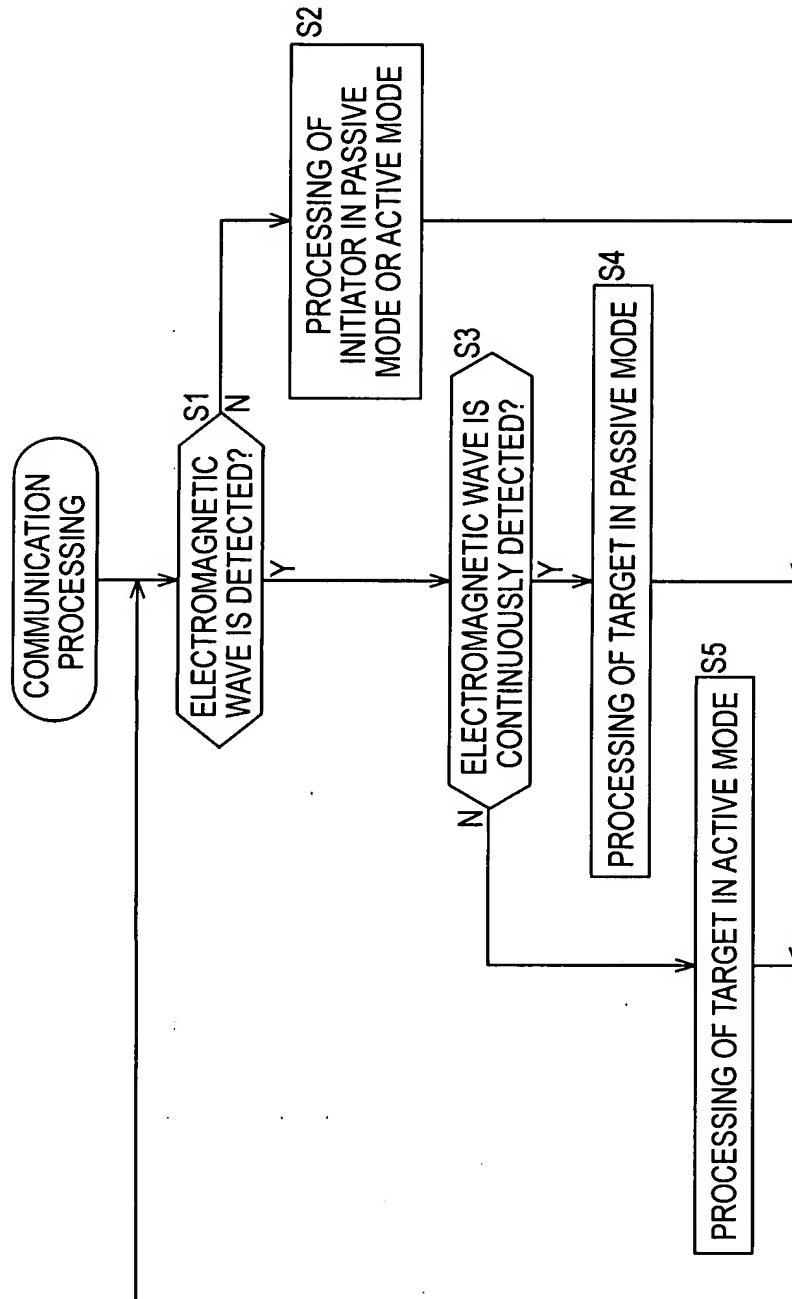
FIG. 12

COMMAND/RESPONSE
ATR_REQ
ATR_RES
WUP_REQ
WUP_RES
PSL_REQ
PSL_RES
DEP_REQ
DEP_RES
DSL_REQ
DSL_RES
RLS_REQ
RLS_RES

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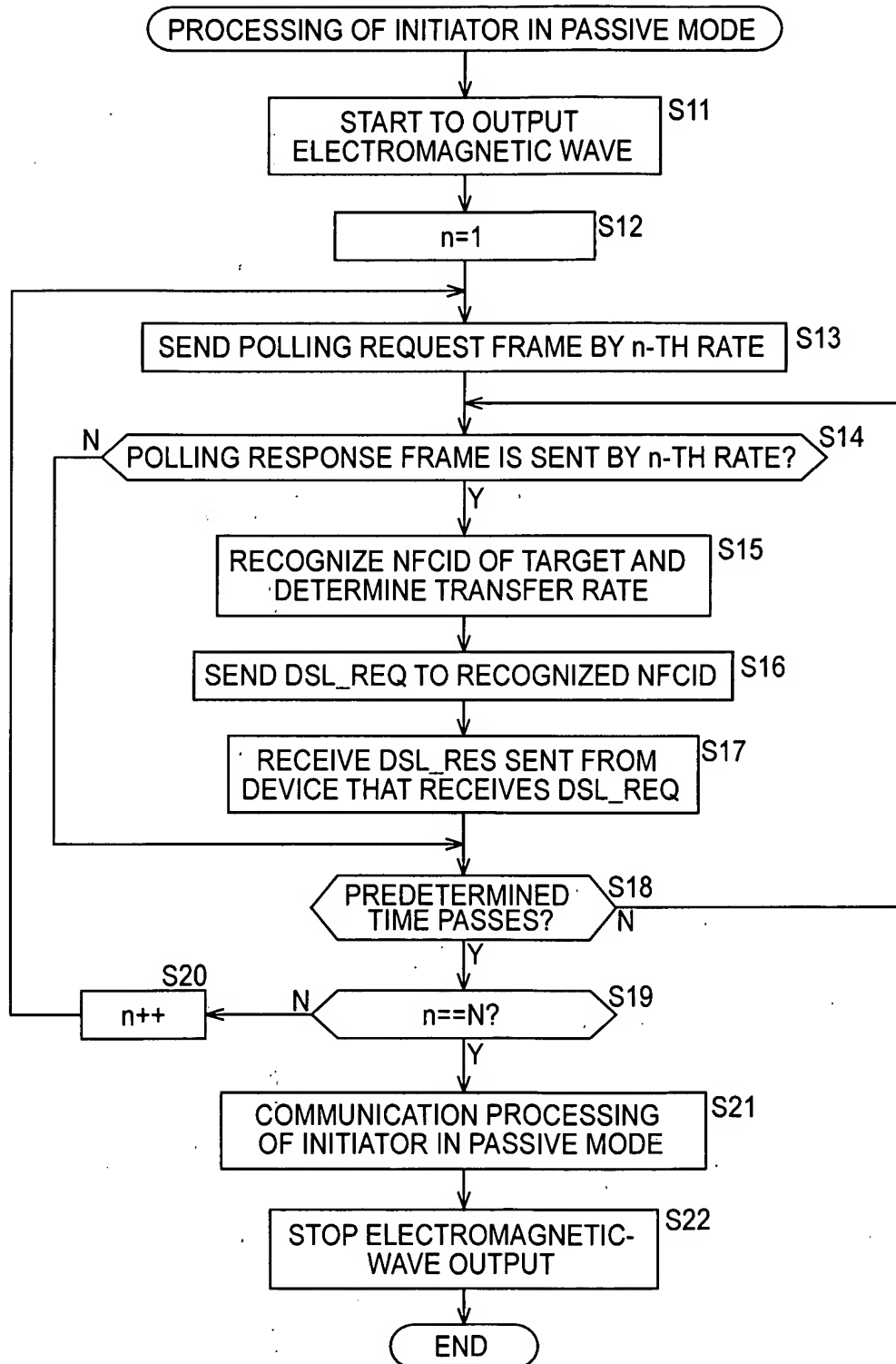
FIG. 13



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FIG. 14



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FIG. 15

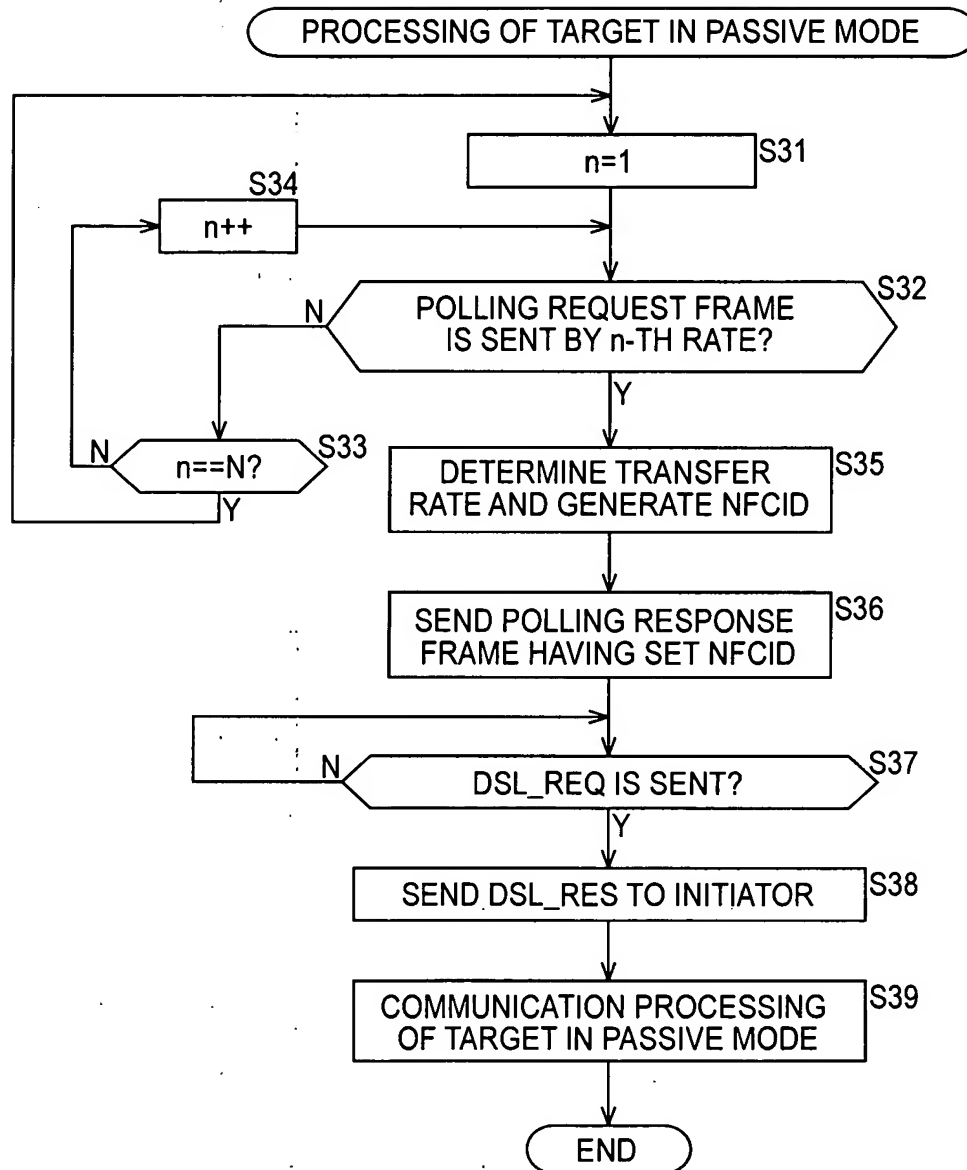
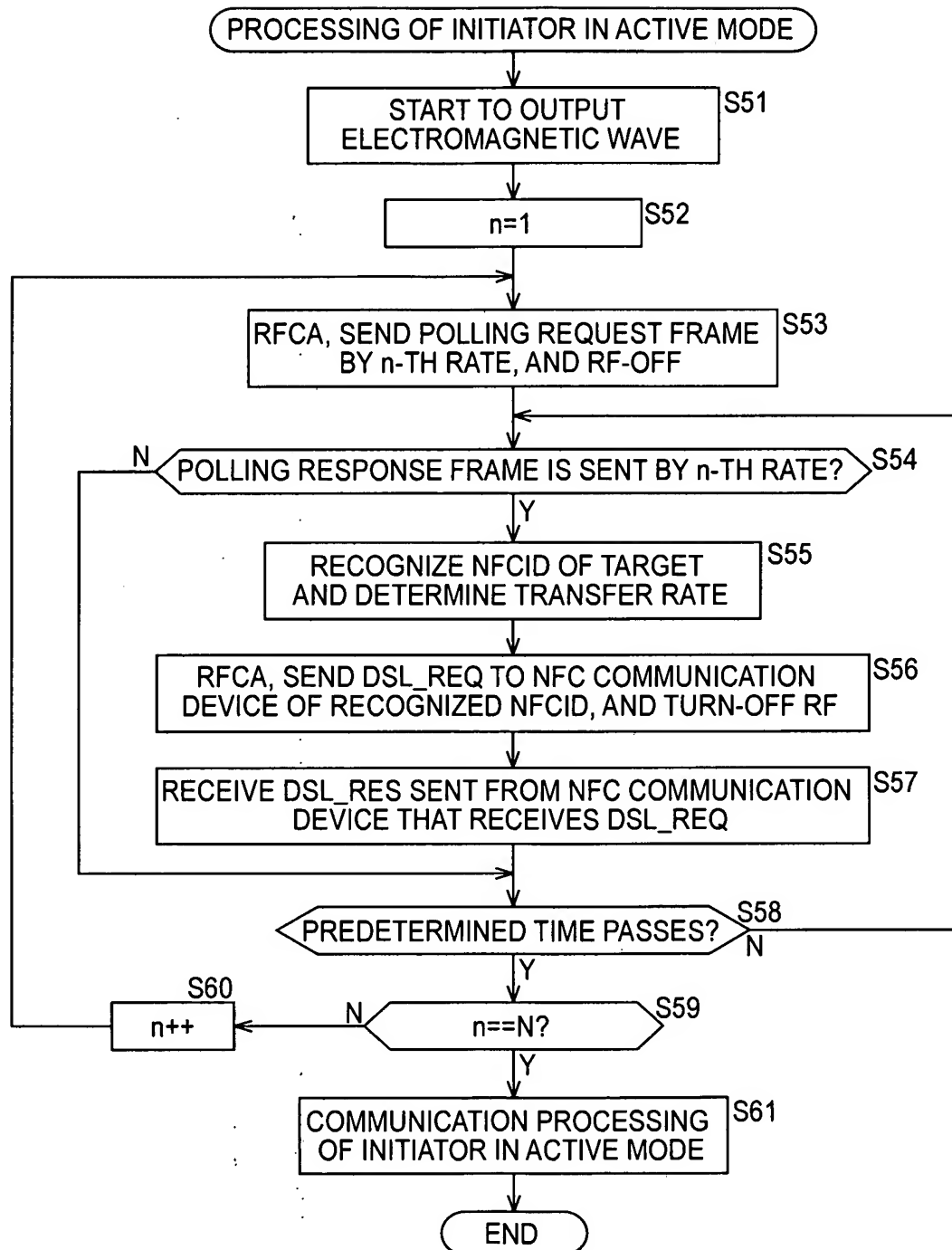


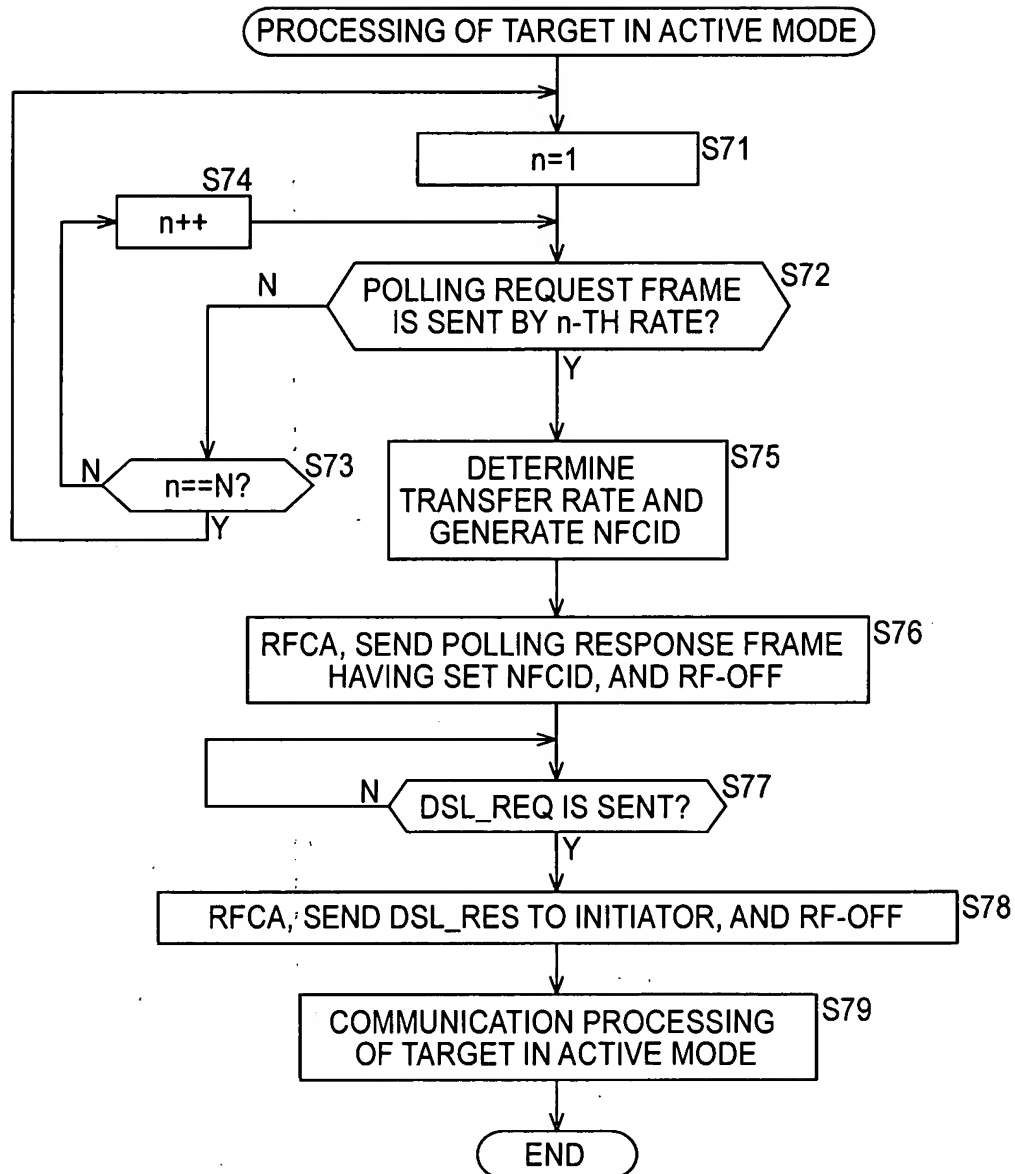
FIG. 16



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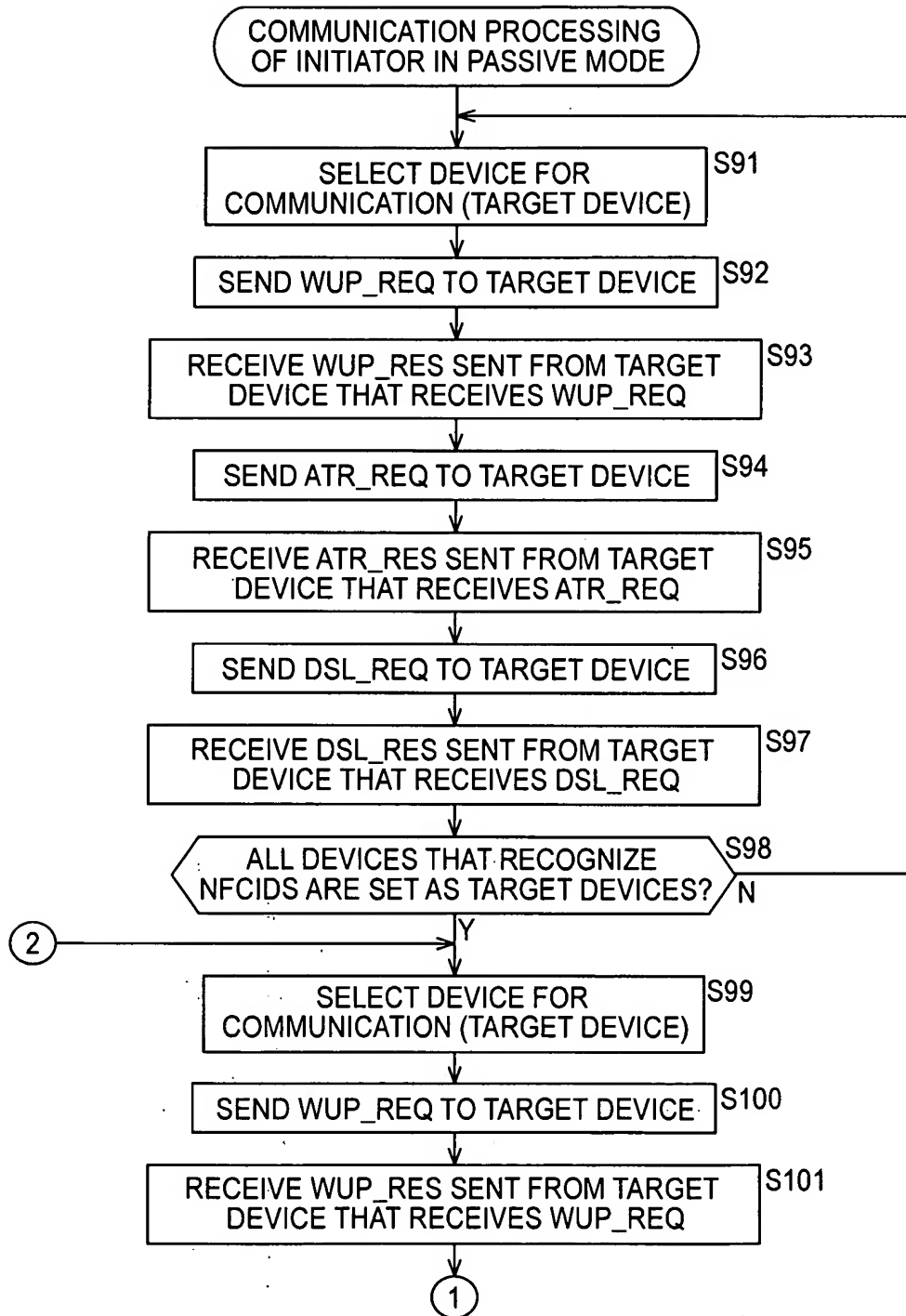
FIG. 17



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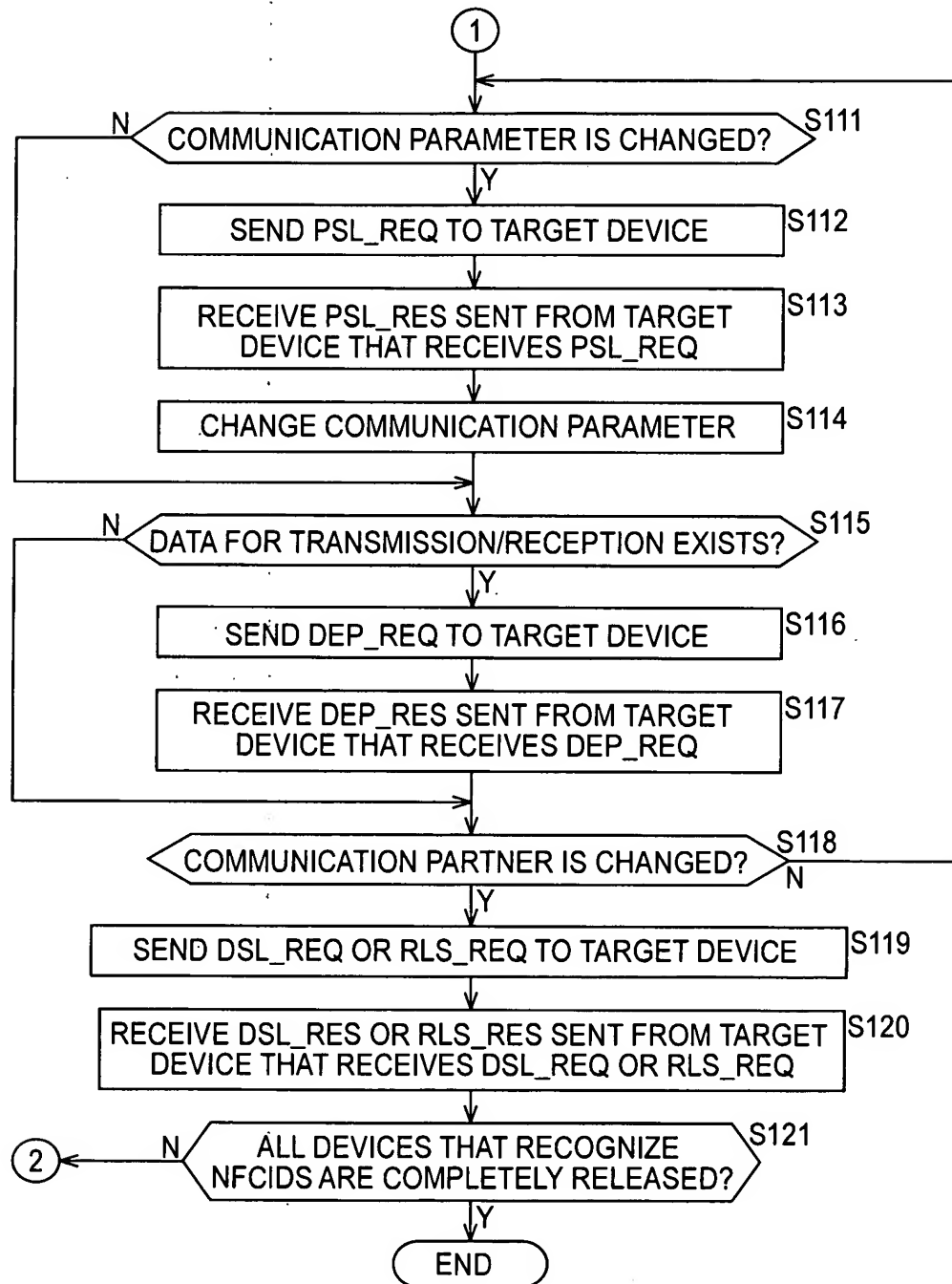
FIG. 18



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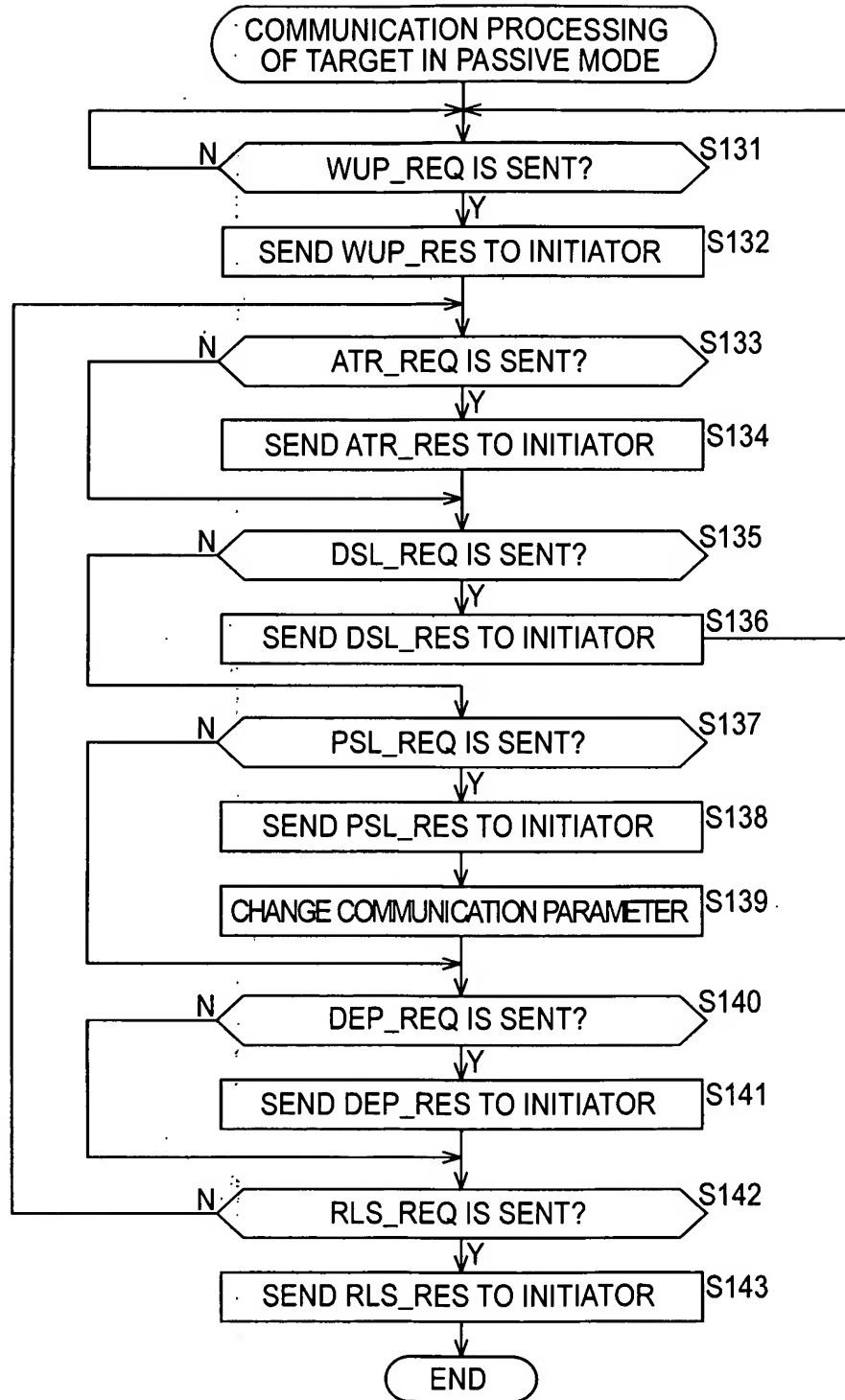
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FIG. 19



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FIG. 20



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FIG. 21

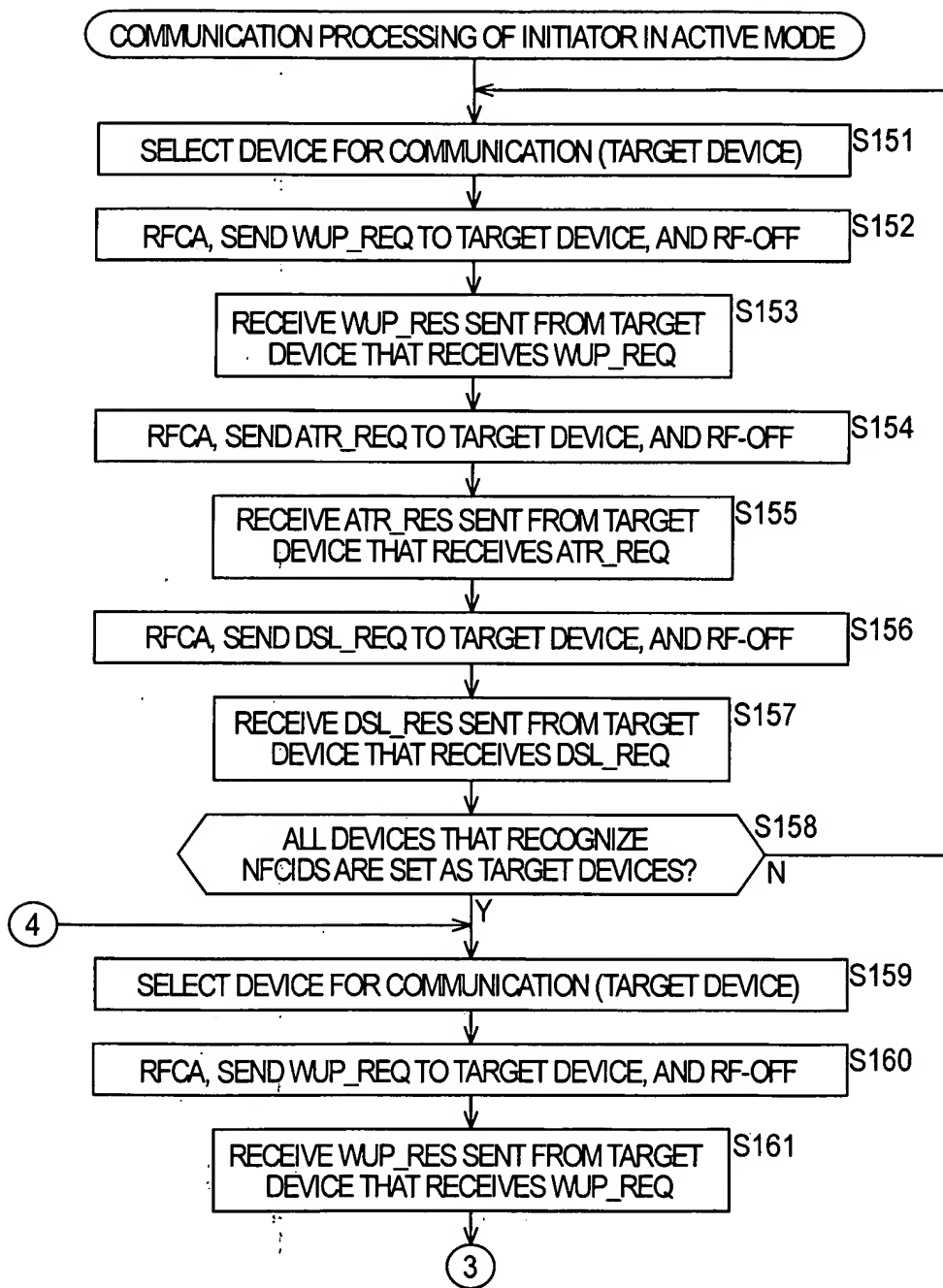
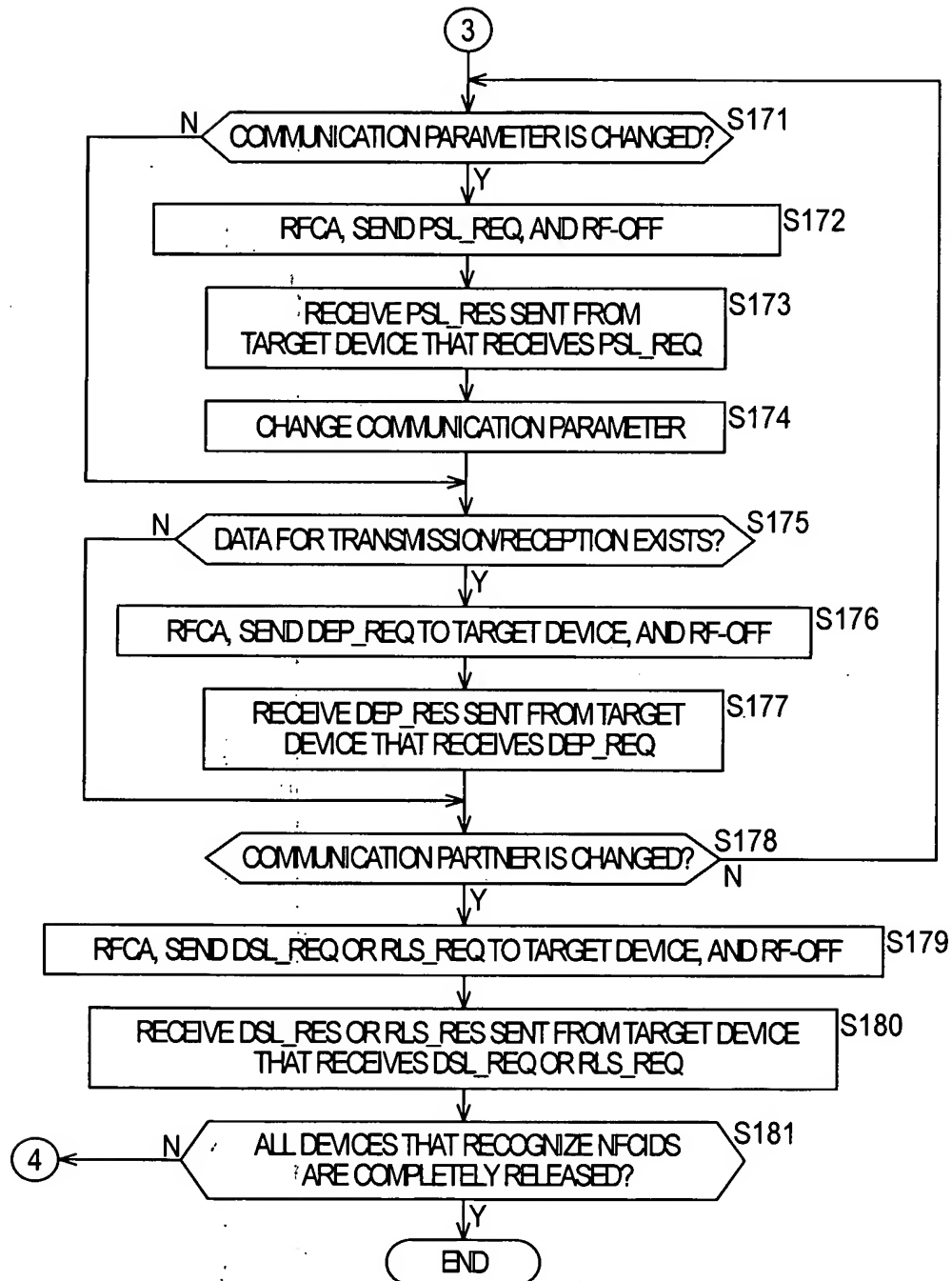


FIG. 22



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FIG. 23

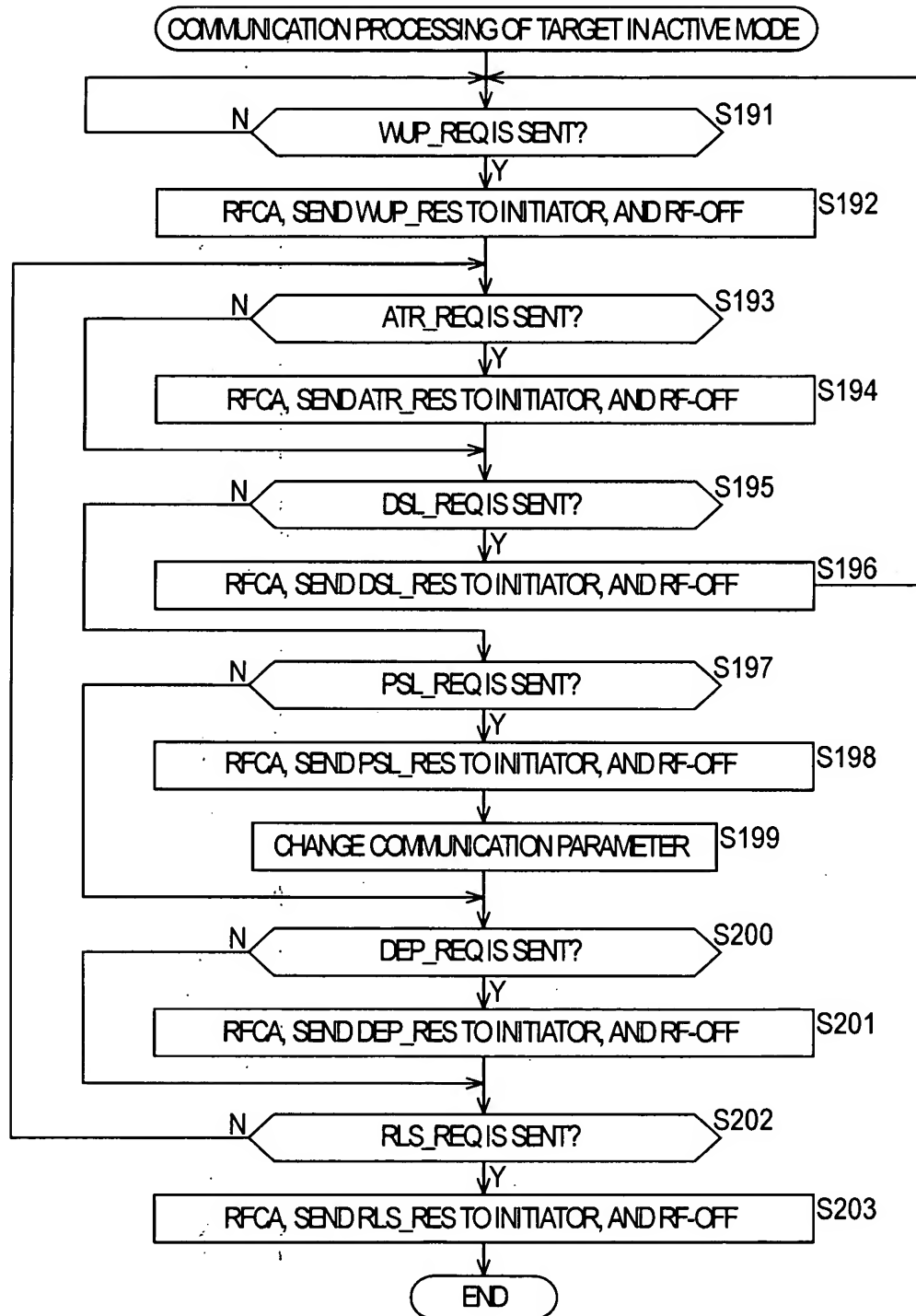


FIG. 24

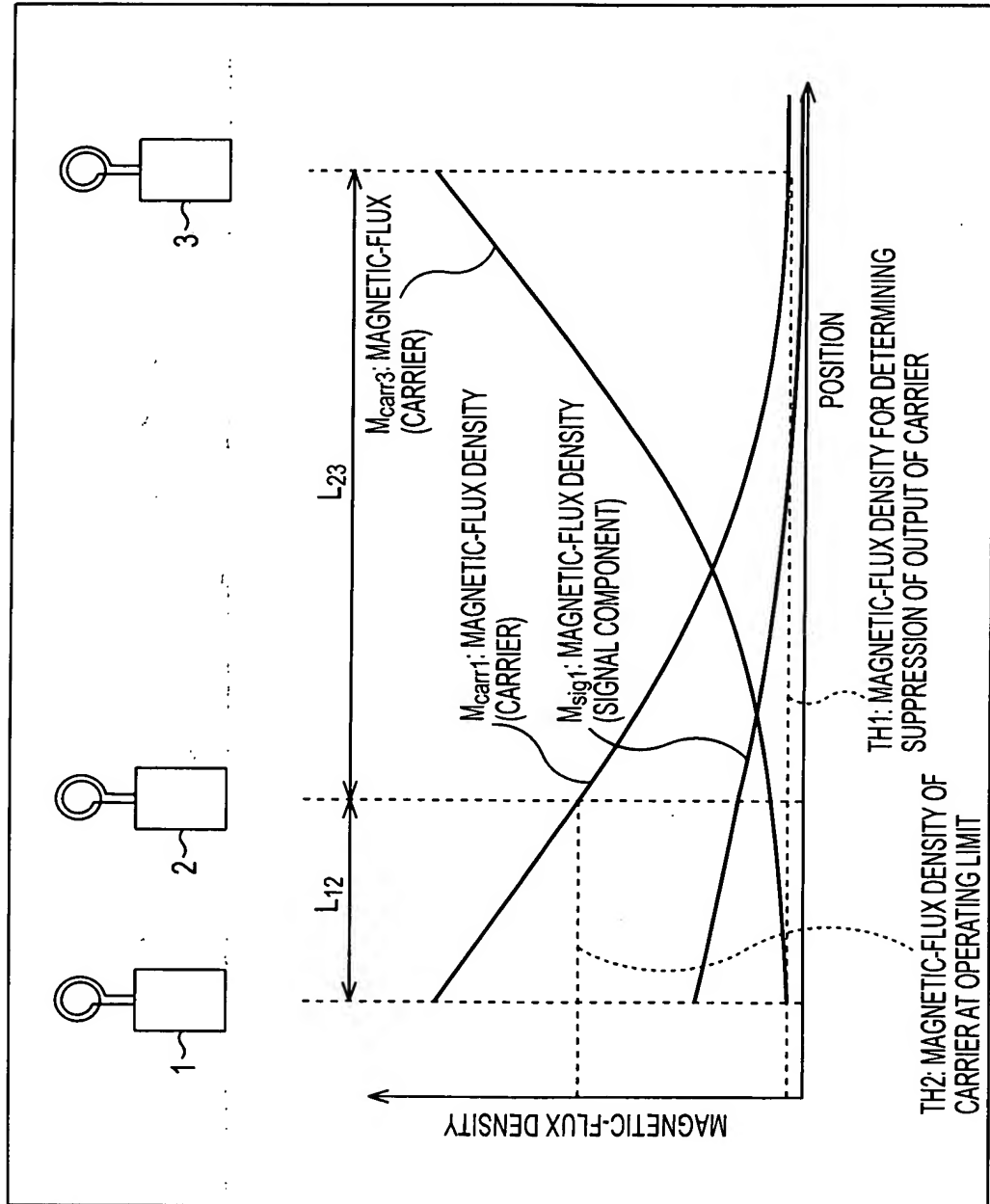


FIG. 25

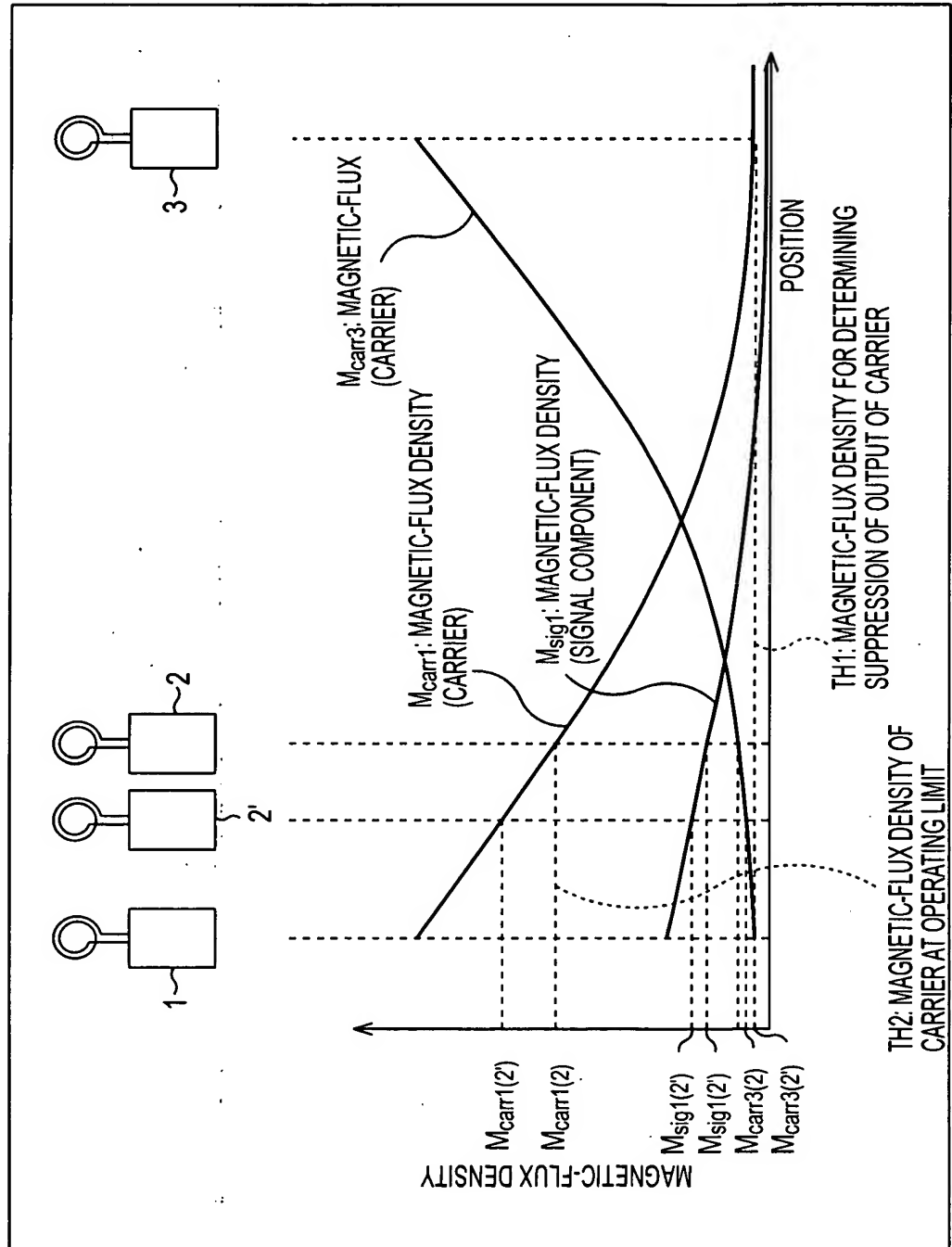


FIG. 26

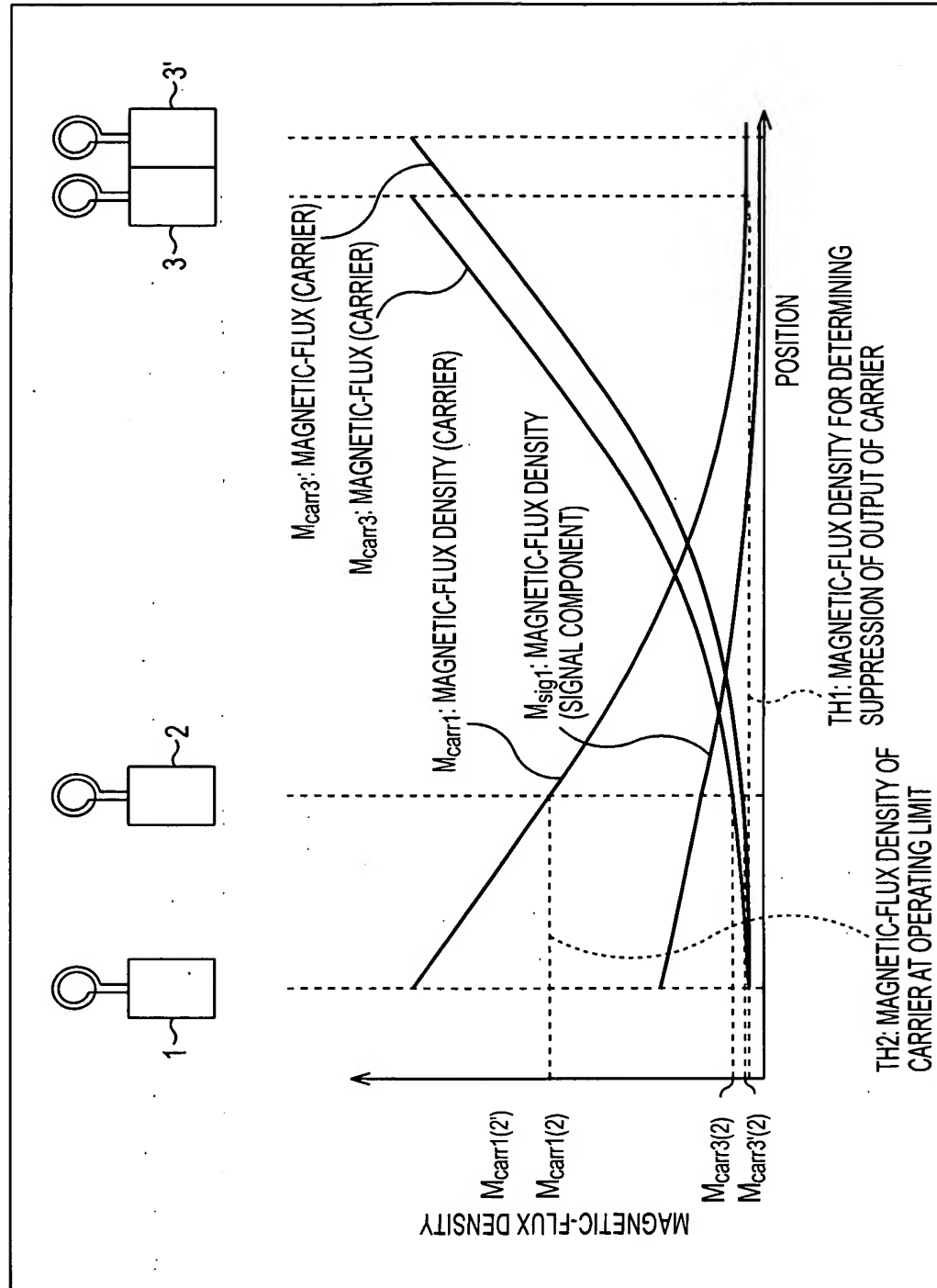
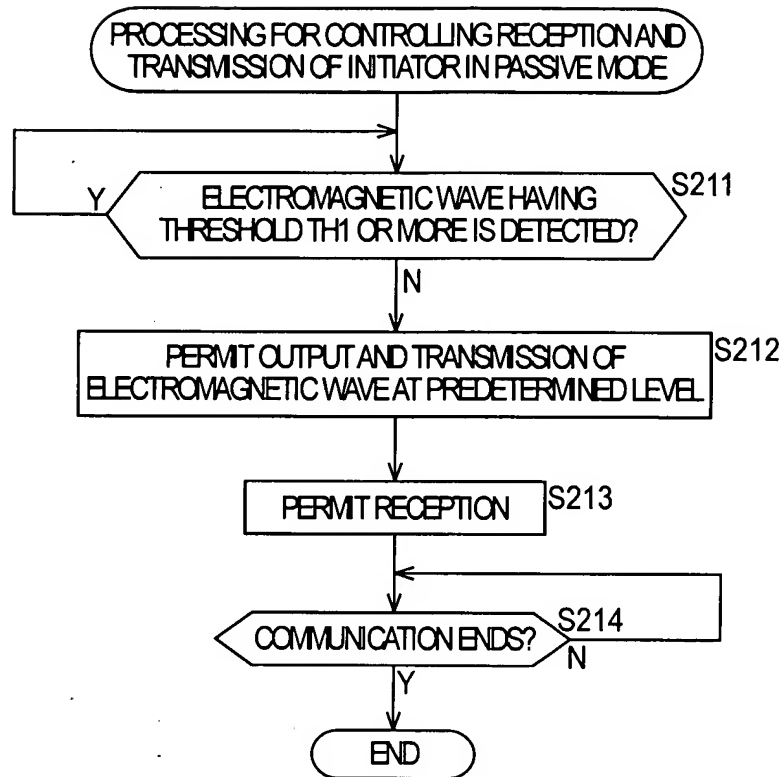


FIG. 27



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FIG. 28

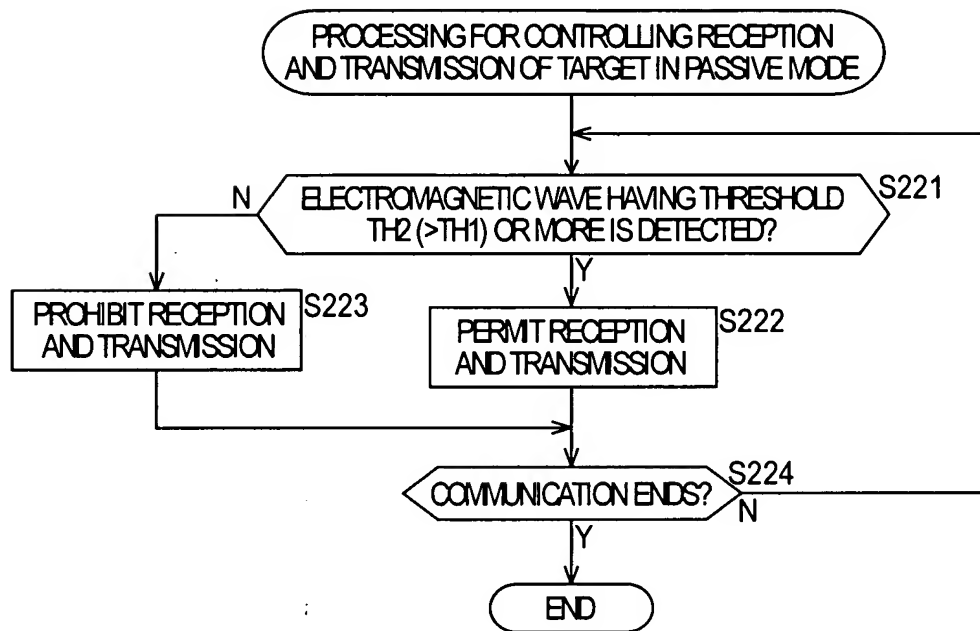
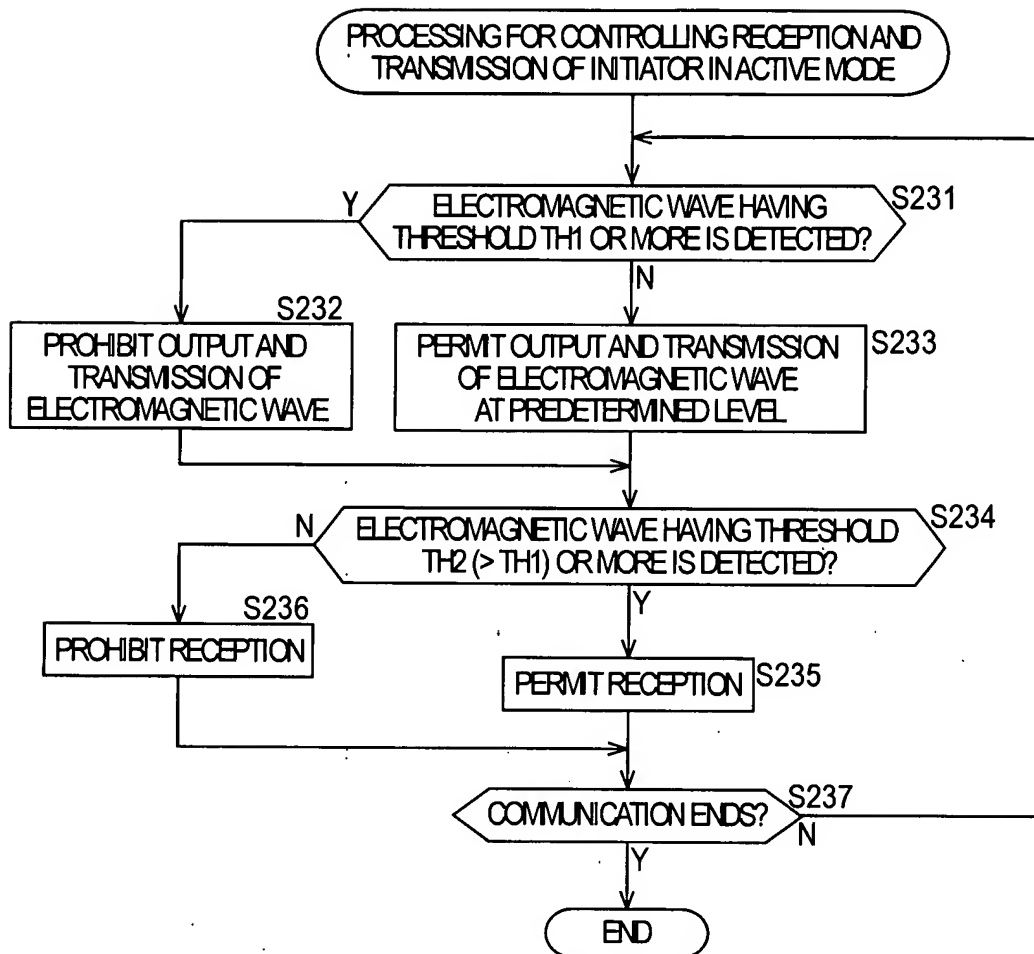


FIG. 29



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FIG. 30

